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Citations: Integrity considerations for secure computer systems ...

... protects integrity by ensuring that **privileged** code is ... Security and Information Flow in **Petri** Net Workflows ... confidentiality, and integrity of **data** [CFMS95] In ... citeseer.ist.psu.edu/context/63953/0 - 52k - Supplemental Result - <u>Cached</u> - <u>Similar pages</u>

Citations: Concurrent Object--Oriented Programming Languages ...

Examples of structured approaches are Monitor [Hoa74] abstract **data** types ... Here is a version of such an account which keeps the **privileged** bank **access**: ... citeseer.ist.psu.edu/context/101862/0 - 23k - <u>Cached</u> - <u>Similar pages</u>

Citations: Detecting Intruders in Computer Systems - Lunt ...

Such intrusions can be detected by audit data [HaC92] generated by the operating ... 1995) uses Colored **Petri** nets to represent intrusion signatures as ... citeseer.ist.psu.edu/context/151145/1819 - 34k - Cached - Similar pages

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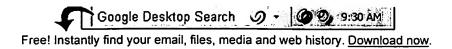
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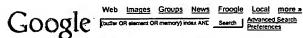
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OIDX lookup cost and optimal use of memory in a ... organizations (context) - Diehr,
Saharia - 1994 10 Index scans using a finite LRU buffer. A validated I/O ...

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Cost-based Selection of Path Expression Processing Algorithms in ...
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The Performance of Concurrent Data Structure Algorithms - Johnson ...
... summary). Abstract: This thesis develops a validated model of ... In a shared memory architecture all of the ... An efficient and high concurrency Index management metho ... citeseer.ist.psu.edu/70869.html - 34k - Supplemental Result - Cached - Similar pages

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Physical Database Design for Data Warehouses - Quass, Database ...
... (context) - Fabret, Regnier et al. - 1993 10 Index scans using a finite Iru buffer:
A validated i/o model (context) - Mackert, Lohman - 1989 8 A ...
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<u>Citations: IEEE Transactions on Knowledge and Data Engineering ...</u>
... leaving the maintenance of the client buffer to the ... we used an efficient secondary memory implementation of a ... data structure [Sam84] as an index developed at ... citeseer.ist.psu.edu/context/51162/0 - 36k - Supplemental Result - <u>Cached - Similar pages</u>

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tis based on the hB tree, a multi attribute point data access method [LS90] ...
citeseer.ist.psu.edu/context/890/0 - 41k - <u>Ceched - Similar pages</u>

Citations: A new approach to rectangle intersections ...

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Citations: Purely Functional Data Structures - Okasaki ...
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[6, 7] use simulation to experiment with more than 100 GB of data... R3000 clocked at 40 MHz, we validated query response time of our simulator again... citeseer.ist.psu.edu/context/60092/455127 - 25k - <u>Cached</u> - <u>Similar pages</u>

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... Virtual Memory Management in Data Parallel Application - Caron, Lazure, ...

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... 1997 6 Sim-alpha: a validated execution driven ... area comparison of embedded DRAM and
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Citations: The OO7 benchmark - Carey, DeWitt, Naughton (ResearchIndex)
The EXODUS client module maintains its own buffer pool on the local machine,
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... for a detailed discussion) Within the portal, authenticated users may ... boundaries, but they only index pages that ... values of its descriptive element ResourceType ... citeseer.ist.psu.edu/context/862193/0 - 31k - Supplemental Result - <u>Cached - Similar pages</u>

<u>Citations: Observations on Optimistic Concurrency Control Schemes ...</u>
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<u>Citations: Parallel Database Systems: The Future of Database ...</u>
... load balancing while reducing access conflicts to the shared memory. ...
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Citations: Nested queries in object bases - Cluet, Moerkotte ...

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... those obtained loading the system with short memory processes [2 ... the peaks thanks to the queue buffer, without introducing ... 2[1,1] is the symmetry Index, fl 0 ... citeseer.ist.psu.edu/context/155669/0 - 43k - Supplemental Result - $\underline{\text{Cached}}$ - $\underline{\text{Similar pages}}$

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Citations: Near neighbor search in large metric spaces - Brin ...
Proceedings of the 21th International Conferenceon Very Large Data Bases, ...
y) 1, where the query has to be exhausthely compared against every element in ...
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Program Checking (ResearchIndex)

Base, leda max, LEDA MEMORY, leda min, leda swap, lifetime of object, ... 0.0: Authentic Data Publication over the Internet - Devanbu, Gertz, Martel. ... citeseer.ist.psu.edu/322135.html - 15k - <u>Cached</u> - <u>Similar pages</u>

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B Pell, SR Sawyer, N Muscettola, B Smith, DE ... - 1998 IEEE Aerospace Conference, Aspen, CO, 1998 - www-aig.jpl.nasa.gov

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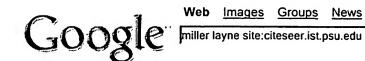
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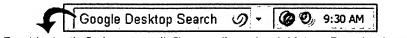
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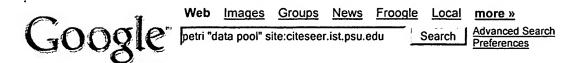
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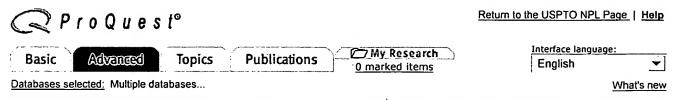
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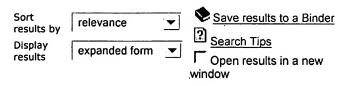


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1 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

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Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

² The Integrated Dictionary/Directory System

Frank W. Allen, Mary E. S. Loomis, Michael V. Mannino

June 1982 ACM Computing Surveys (CSUR), Volume 14 Issue 2

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Lance J. Hoffman

June 1969 ACM Computing Surveys (CSUR), Volume 1 Issue 2

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2 Programming languages and systems for prototyping concurrent applications Wilhelm Hasselbring

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Concurrent programming is conceptually harder to undertake and to understand than sequential programming, because a programmer has to manage the coexistence and coordination of multiple concurrent activities. To alleviate this task several high-level approaches to concurrent programming have been developed. For some high-level programming approaches, prototyping for facilitating early evaluation of new ideas is a central goal. Prototyping is used to explore the ...

Keywords: concurrency, distribution, parallelism, rapid prototyping, very high-level languages

3 Concepts of building a design automation system

D. Lyons, M. McLuhan January 1966 **Proceedings of the SHARE design automation project**

Full text available: pdf(1.34 MB) Additional Information: full citation, abstract, references, index terms

"....automation creates roles for people, which is to say depth of involvement in their work and human association that our preceding mechanical technology had destroyed." There are many terms used for the computer application to the aid of design and production, none of which truly state the case. We are concerned here with a system that will implement the ideas behind these terms. The tools used for this implementation are the computers and a large amount of softwar ...

4 A rapid railway simulation model development system incorporating automatic model generation

Kyriakos Tsiflakos, Stephen C. Mathewson

December 1991 Proceedings of the 23rd conference on Winter simulation

Full text available: pdf(957.93 Additional Information: full citation, references, index terms KB) The knowledge in multiple human relevance judgments W. John Wilber April 1998 ACM Transactions on Information Systems (TOIS), Volume 16 Issue 2 Full text available: pdf(226.52 Additional Information: full citation, abstract, references, citings, index terms We show first that the pooling of multiple human judgments of relevance provides predictor of relevance that is superior to that obtained from a single human's relevance judgemts. A learning algorithm applied to a set of relevance judgments obtained from a single human would be expected to perform on new material at a level somewhat below that human. However, we examine two learning methods which when trained on the superior source of pooled human relevance judgments are able to perform at ... Keywords: document retrieval test set construction, human relevance judgments, inverse document frequency weights, log odds of relevance, minimal-length least-square fit, vector model, word weights 6 Web mining and clustering: Stylistic and lexical co-training for web block classification Chee How Lee, Min-Yen Kan, Sandra Lai November 2004 Proceedings of the 6th annual ACM international workshop on Web information and data management Full text available: pdf(349.91 Additional Information: full citation, abstract, references, index terms KB) Many applications which use web data extract information from a limited number of regions on a web page. As such, web page division into blocks and the subsequent block classification have become a preprocessing step. We introduce PARCELS, an open-source, co-trained approach that performs classification based on separate stylistic and lexical views of the web page. Unlike previous work, PARCELS performs classification on fine-grained blocks. In addition to table-based layout, the system handl ... Keywords: PARCELS, co-training, lexical and stylistic learners, web page block classification, web page division 7 Efficient algorithms for processing XPath queries Georg Gottlob, Christoph Koch, Reinhard Pichler June 2005 ACM Transactions on Database Systems (TODS), Volume 30 Issue 2 Full text available: pdf(721.61 Additional Information: full citation, abstract, references, index terms Our experimental analysis of several popular XPath processors reveals a striking fact: Query evaluation in each of the systems requires time exponential in the size of queries in the worst case. We show that XPath can be processed much more efficiently,

and propose main-memory algorithms for this problem with polynomial-time combined query evaluation complexity. Moreover, we show how the main ideas of our algorithm can be profitably integrated into existing XPath processors. Finally, we present ...

Keywords: Efficient Algorithms, XML, XPath

8 Bug isolation via remote program sampling Ben Liblit, Alex Aiken, Alice X. Zheng, Michael I. Jordan

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	Full text available: pdf(258.37 Additional Information: full citation, abstract, references, citings, index
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	We propose a low-overhead sampling infrastructure for gathering information from the executions experienced by a program's user community. Several example applications illustrate ways to use sampled instrumentation to isolate bugs. Assertion-dense code can be transformed to share the cost of assertions among many users. Lacking assertions, broad guesses can be made about predicates that predict program errors and a process of elimination used to whittle these down to the true bug. Finally, even
	Keywords : assertions, bug isolation, feature selection, logistic regression, random sampling, statistical debugging
9	A history of the Promis technology: an effective human interface Jan Schultz
	January 1986 Proceedings of the ACM Conference on The history of personal workstations
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10	Status of work toward revision of programming language Fortran
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11	Deductive inference: question answering/theorem proving: A deductive model of
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	The efficiency of problem-solving computer programs, has suffered from such programs willingness to search problem spaces blindly and their inability to accept new information that would help. A new approach is described, in which the problem solver is driven by a theorem prover which retrieves and chooses among solution plans. The theorem prover can accept new deductive rules expressed in the problem solver's control vocabulary. In this way, the advantages of determinism and extensibility are a
12	The implementation of expert systems: a survey of successful implementations
	Craig K. Tyran, Joey F. George January 1993 ACM SIGMIS Database, Volume 24 Issue 1
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	A descriptive mail survey of managers of work groups using expert system (ES) technology was performed to gain insight into the management of the ES implementation process. This article presents survey results representing 45 different ES projects that have been successfully implemented in organizations. Each of the managers was asked to evaluate the importance of 13 factors to ES implementation

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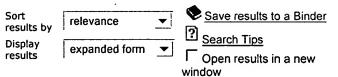
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1 ITS: a tool for rapidly developing interactive applications Charles Wiecha, William Bennett, Stephen Boies, John Gould, Sharon Greene July 1990 ACM Transactions on Information Systems (TOIS) Volume 8 Issue 3

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The ITS architecture separates applications into four layers. The action layer implements back-end application functions. The dialog layer defines the content of the user interface, independent of its style. Content specifies the objects included in each frame of the interface, the flow of control among frames, and what actions are associated with each object. The style rule layer defines the presentation and behavior of a family of interaction techniques. Finally, the style program layer i ...

2 Efficient algorithms for processing XPath queries

Georg Gottlob, Christoph Koch, Reinhard Pichler

June 2005 ACM Transactions on Database Systems (TODS), Volume 30 Issue 2

Full text available: pdf(721.61 KB) Additional Information: full citation, abstract, references, index terms

Our experimental analysis of several popular XPath processors reveals a striking fact: Ouery evaluation in each of the systems requires time exponential in the size of queries in the worst case. We show that XPath can be processed much more efficiently, and propose main-memory algorithms for this problem with polynomial-time combined query evaluation complexity. Moreover, we show how the main ideas of our algorithm can be profitably integrated into existing XPath processors. Finally, we present ...

Keywords: Efficient Algorithms, XML, XPath

3 The S/Net's Linda kernel

Nicholas Carriero, David Gelernter

May 1986 ACM Transactions on Computer Systems (TOCS), Volume 4 Issue 2

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.55 MB)

Linda is a parallel programming language that differs from other parallel languages in its simplicity and in its support for distributed data structures. The S/Net is a multicomputer, designed and built at AT&T Bell Laboratories, that is based on a fast, word-parallel bus interconnect. We describe the Linda-supporting communication kernel we have implemented on the S/Net. The implementation suggests that Linda's unusual shared-memory-like communication primitives can be made to run well in ...

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4 Programming languages and systems for prototyping concurrent applications Wilhelm Hasselbring



March 2000 ACM Computing Surveys (CSUR), Volume 32 Issue 1

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Concurrent programming is conceptually harder to undertake and to understand than sequential programming, because a programmer has to manage the coexistence and coordination of multiple concurrent activities. To alleviate this task several high-level approaches to concurrent programming have been developed. For some high-level programming approaches, prototyping for facilitating early evaluation of new ideas is a central goal. Prototyping is used to explore the ...

Keywords: concurrency, distribution, parallelism, rapid prototyping, very high-level languages

5 A history of the Promis technology: an effective human interface

January 1986 Proceedings of the ACM Conference on The history of personal workstations

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Scientific computing systems for individuals were pioneered early at Hewlett-Packard, beginning with the 9100A Desktop Calculator in 1968. Extensions of this first machine were soon seen in Personal Peripherals, such as Printers, Tape Cartridges, and Plotters, and followed by Graphic CRT Displays. By early 1972, the Desktop unit had been augmented by a very powerful Pocket Calculator, the ground-breaking HP 35AThis paper traces the evolution of these machines to the present day, ...

6 Window real objects: a distributed shared memory for distributed implementation of **GUI** applications



Noboru Koshizuka, Ken Sakamura

December 1993 Proceedings of the 6th annual ACM symposium on User interface software and technology

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Keywords: BTRON, distributed shared memory, graphical user interface, multiuser interface, parallel programming, window system

7 The information age and the printing press: looking backward to see ahead James A. Dewar

August 2000 Ubiquity, Volume 1 Issue 25

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8 Link-time optimization of ARM binaries

Bruno De Bus, Bjorn De Sutter, Ludo Van Put, Dominique Chanet, Koen De Bosschere June 2004 ACM SIGPLAN Notices, Proceedings of the 2004 ACM SIGPLAN/SIGBED conference on Languages, compilers, and tools for embedded systems Volume 39 Issue 7

Full text available: pdf(797.20 KB) Additional Information: full citation, abstract, references, citings, index

The overhead in terms of code size, power consumption and execution time caused by the use of precompiled libraries and separate compilation is often unacceptable in the embedded world, where real-time constraints, battery life-time and production costs are of critical importance. In this paper we present our link-time optimizer for the ARM architecture. We discuss how we can deal with the peculiarities of the ARM architecture

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related to its visible program counter and how the introduced overhea ...

Keywords: compaction, linker, optimization, performance

9 Performance of the VAX-11/780 translation buffer: simulation and measurement Douglas W. Clark, Joel S. Emer February 1985 ACM Transactions on Computer Systems (TOCS), Volume 3 Issue 1



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A virtual-address translation buffer (TB) is a hardware cache of recently used virtual-to-physical address mappings. The authors present the results of a set of measurements and simulations of translation buffer performance in the VAX-11/780. Two different hardware monitors were attached to VAX-11/780 computers, and translation buffer behavior was measured. Measurements were made under normal time-sharing use and while running reproducible synthetic time-sharing work loads. Reported measure ...

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access to data, and to cache data, pool database connections, and reuse data ...

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... The random data pool is updated with unpredictable process ... example you can delete

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Search strategy

No.	Database	Search term	Info added since	Results
1	INZZ	data ADJ pool	unrestricted	87
2	INZZ	buffer ADJ index	unrestricted	3
3	INZZ	authenticat\$ ADJ (connection OR request OR query OR access)	unrestricted	190
4	INZZ	validat\$ ADJ (connection OR request OR query OR access)	unrestricted	34
5	INZZ	data ADJ (access OR transmission) NEAR (authentic\$ OR valid\$)	unrestricted	34
6	INZZ	1 AND 5	unrestricted	0
7	INZZ	1 AND 3	unrestricted	0
8	INZZ	1 AND 4	unrestricted	0
9	INZZ	1 AND 2	unrestricted	0
10	INZZ	3 AND 5	unrestricted	2
11	INZZ	4 AND 5	unrestricted	. 0
12	INZZ	connection ADJ validation	unrestricted	4
13	4 AND data ADJ (access OR transmission OR request OR query)		unrestricted	1
14	INZZ	data ADJ access	unrestricted	3893
15	INZZ	data ADJ (access OR transmission OR request OR query)		23706
16	INZZ	15 AND handle NEAR (flag OR index OR version OR code OR key)	unrestricted	1
17	INZZ	15 AND handle NEAR (flag OR index OR version OR code OR key)	unrestricted	1

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